

Figure 1

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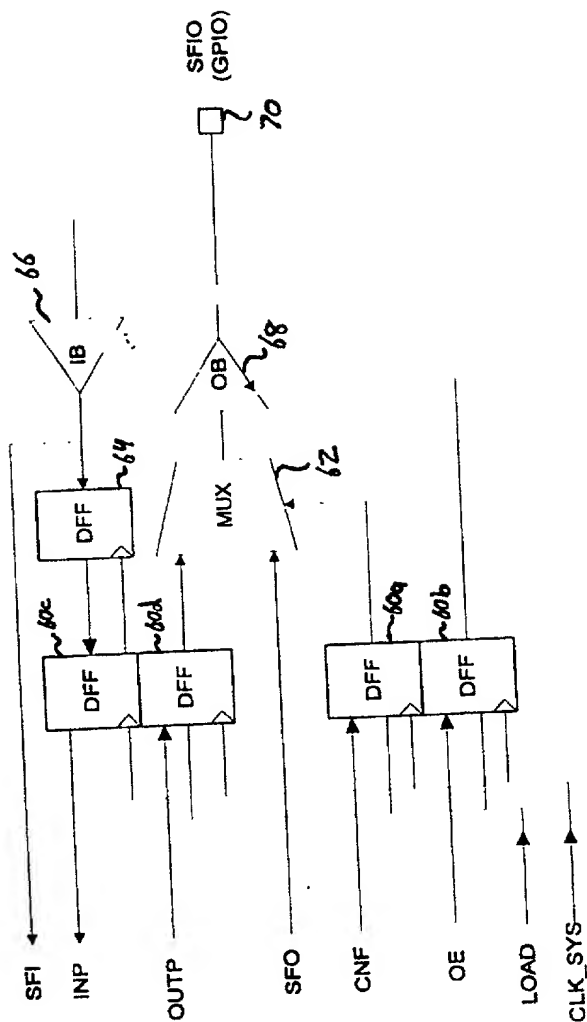
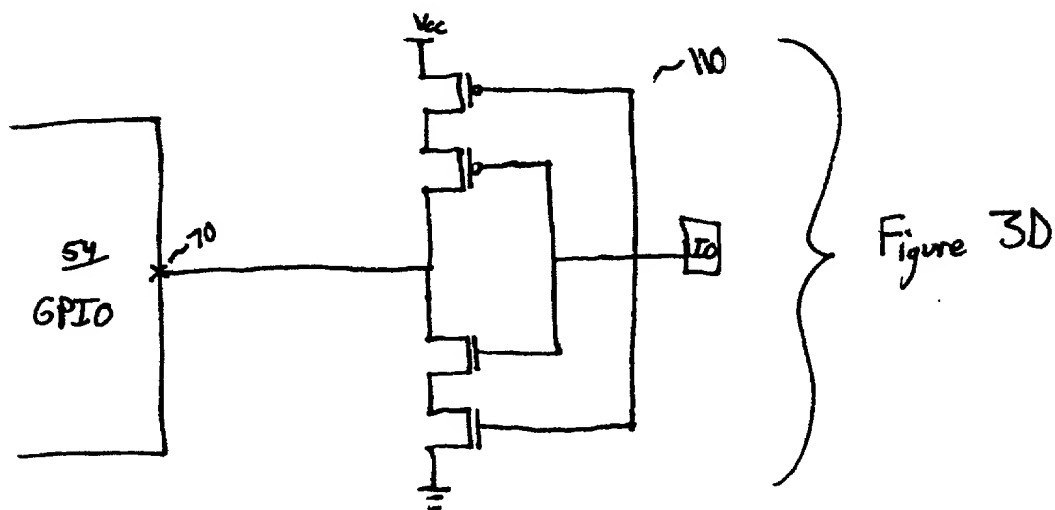
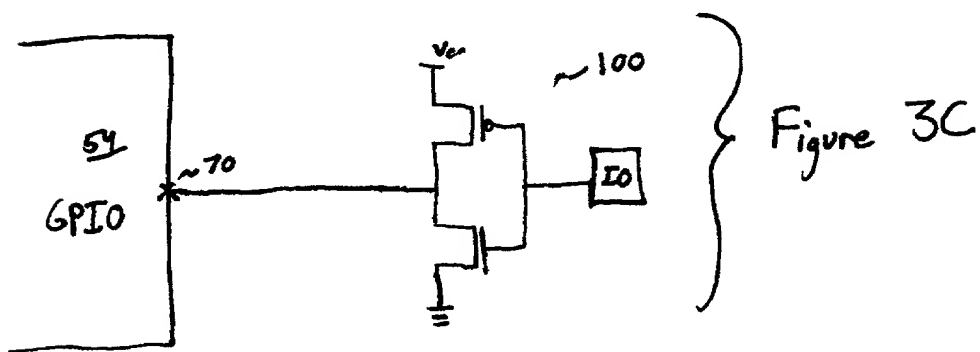
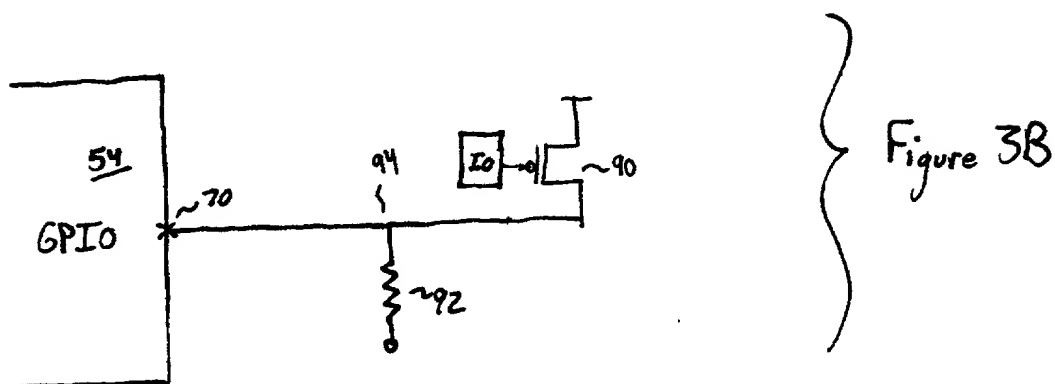
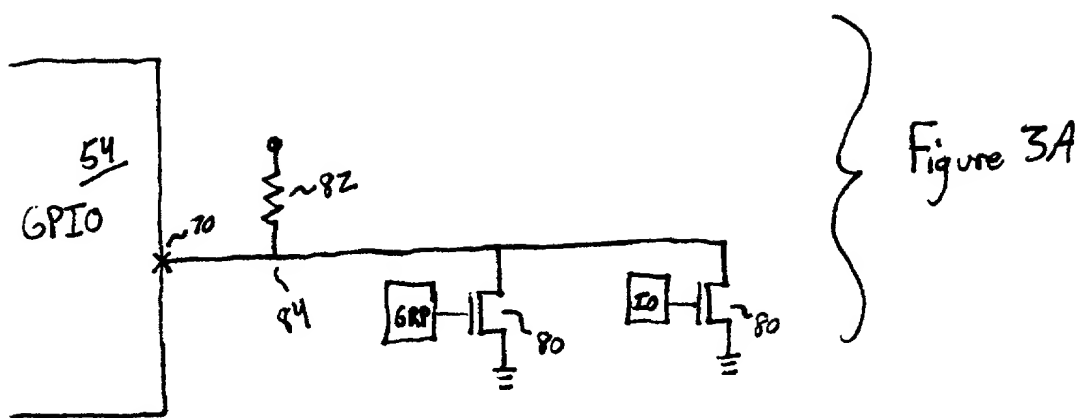


Figure 2



CFG-REG NAME	ADDRESS	TYPE	SIZE	DESCRIPTION	7	6	5	4	3	2	1	0
GPIO Controller												
120 ~ GPIO_CNFR	CF00:0000	R/W	8b	GPIO/SPIO selection	PORT-A. ENB							
122 ~ GPIO_OR	CF00:0010	R/W	8b	GPIO Output Enable	PORT-A. OE							
124 ~ GPIO_OUT_REG	CF00:0020	R/W	8b	GPIO Output Port Register	PORT-A. OUT							
126 ~ GPIO_IN_REG	CF00:0030	R/O	8b	GPIO Input Port Register	PORT-A. IN							
128 ~ GPIO_INT_STA	CF00:0040	R/O	8b	GPIO Interrupt Status Register	PORT-A. INT. STA							
130 ~ GPIO_INT_ENB	CF00:0050	R/W	8b	GPIO Interrupt Enable Register	PORT-A. INT. ENB							
132 ~ GPIO_INT_LVL	CF00:0060	R/W	8b	GPIO Interrupt Activation Level	PORT-A. INT. LVL							
134 ~ GPIO_INT_CLR	CF00:0070	clr	8b	GPIO Interrupt Flag Set-to-Clear	PORT-A. INT. CLR							

Figure 4